timer set time period

3/9

6/8 km

FIG. 3

low pressure CVD system

host computer

request for loading wafer lots (S501)

notice of the lots to be processed (S502)
(Lot ID: A, B, C)

request for loading wafer lots (A, B) (S503)

instruct to output and carry lots (A, B) (S504)

request for loading wafer lot (C) (S505)

instruct to output and carry lot (C) (S504, S506)

request for inhibiting the load (S505)

uprior art

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